

7035245 c/c

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No. : 10/083,655
Applicant : ORR, et al.
Filed : February 27, 2002
TC/A.U. : 2665
Examiner : Ho, Duc Chi
Confirmation No. : 9478
Docket No. : 0918.0145C
Customer No. : 27896
Title : Methods and Apparatus for Multiplexing Signal
Codes via Weighted Majority Logic

TRANSMITTAL LETTER

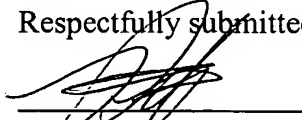
COMMISSIONER FOR PATENTS
P.O. Box 1450
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Sir:

Transmitted herewith is a Certificate of Correction (1 total page).

It is believed that no fees are required at this time, as the error in Official Letters Patent was the fault of the U.S. Patent and Trademark Office. However, Applicants hereby petition for any extension of time that may be necessary to maintain the pendency of this application. The Commissioner is hereby authorized to charge payment of any additional fees required for the above-identified application or credit any overpayment to Deposit Account No. 05-0460.

Respectfully submitted,


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Certificate
MAY 24 2006
of Correction

MAY 25 2006

**UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION**

Sheet 1 of 1

PATENT NO. : 7,035,245 B2
DATED : Apr. 25, 2006
INVENTOR(S) : Orr et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

22. The apparatus of claim 16, wherein said majority logic unit weights the values of the plurality of signals in accordance with a power allocation associated with the plurality of signals.

42. The apparatus of claim 36, wherein said means for weighting weights the values of the plurality of signals in accordance with a power allocation associated with the plurality of signals.

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PATENT NO. 7,035,245 B2

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MAY 25 2006